

The clock IC SDL9051 is a low-threshold-voltage, ion-implanted metal gate CMOS integrated circuit that provides signals to drive a 4-bit duplex liquid crystal display with colon, PM/AM-Time mark and Chime mark.

FUNCTIONS

- 5 Functions: month, date, hour, minute, second
- Setting alarm time with/without 4.5min snooze
- 30-second alarm sound
- Chime on every hour
- User selectable 12-hour/24-hour format
- 4 year calendar
- One-touch correction of time error within ± 30 seconds
- Alarm, chime enable/disable operation
- 2-switch sequential operation
- LCD test

FEATURES

- Single-chip CMOS construction
- Drives 4-bit duplex LCD with PM/AM-Time, alma mark and chime mark
- Colon display
- Direct drive of piezoelectric transducer
- Low power dissipation
- 32,768Hz crystal frequency
- On-chip oscillator, capacitor and resistors
- On-chip voltage doublers
- Single 1.5V battery operation
- Denounce circuitry on switch inputs
- Protection against static discharge

ABSOLUTE MAXIMUM RATINGS

(Ta = 25°C)

Characteristic	Symbol	Value	Unit
Supply voltage (0V-Vcc)	V _{DS}	-0.3 ~ +2.0	V
Supply voltage (0V-VOCC2)	V _{DE}	-0.3 ~ +4.0	V
Operating temperature	Topr	-20 ~ +75	°C
Storage temperature	Tstg	-55 ~ +125	°C

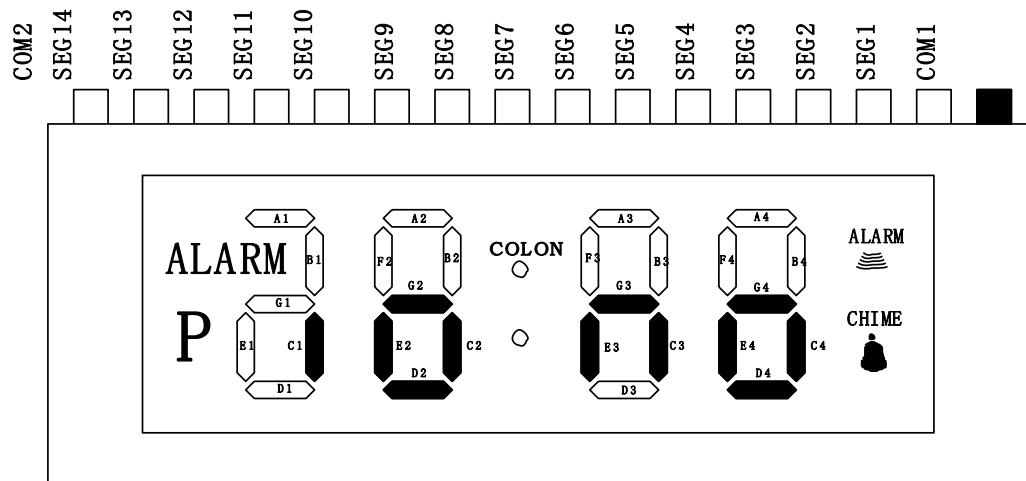
* Voltage greater than above may result in damage the circuit

* SUBSTRATE should be connected with Vdd

ELECTRICAL CHARACTERISTICS (Ta = 25°C, V_{DD} = 0V, V_{SS} = 1.5V; unless otherwise specified)

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Item	Conditions	Symbol	Min	Typ	Max	Unit
Operation voltage	-	V _{SS}	1.2	1.5	1.8	V
	-	V _{EE}	2.4	3.0	3.6	V
Supply current	Without load	I _{DD}	-	1.0	2.0	uA
Input high voltage	-	V _{IH}	V _{DD} -0.3	-	V _{DD}	V
Input low voltage	-	V _{IL}	V _{SS}	-	V _{SS} +0.3	V
Switch activation current	V _{IN} = V _{DD}	I _{sw}	0.1	5.0	10.0	uA
Oscillator start voltage	Sithin 5 sec	V _{osc}	-	-	1.45	V
Oscillator stop voltage	-	V _{osp}	-	-	1.15	V
Alarm drive current	V _{SAT} = 0.5V (Both direction)	I _{ALA}	0.5	2.0	-	mA
Oscillator frequency	-	F _{osc}	-	32.768	-	Hz
DC-DC conversion frequency	C1 = C2 = 0.2uF	F _{con}	-	1.024	-	Hz
LCD frequency	-	F _D	-	32	-	Hz
Oscillator input capacitor	-	C _{IN}	-	20	-	pF
Time stability	V _{SS} = -1.3-1.8 (C _{OUT} = 25pF)	T _{STB}	-	1	3	ppm



PIN	COM2	SEG14	SEG13	SEG12	SEG11	SEG10	SEG9	SEG8
COM1	/	PM	C1	D2	E2	G2	C2	D4
COM2	COM2	AL	ADEG1	B1	F2	A2	B2	COLON
PIN	SEG7	SEG6	SEG5	SEG4	SEG3	SEG2	SEG1	COM1
COM1	E3	G3	C3	E4	G4	C4	CHIME	COM1
COM2	F3	AD3	B3	F4	A4	B4	ALARM	/

Fig.1

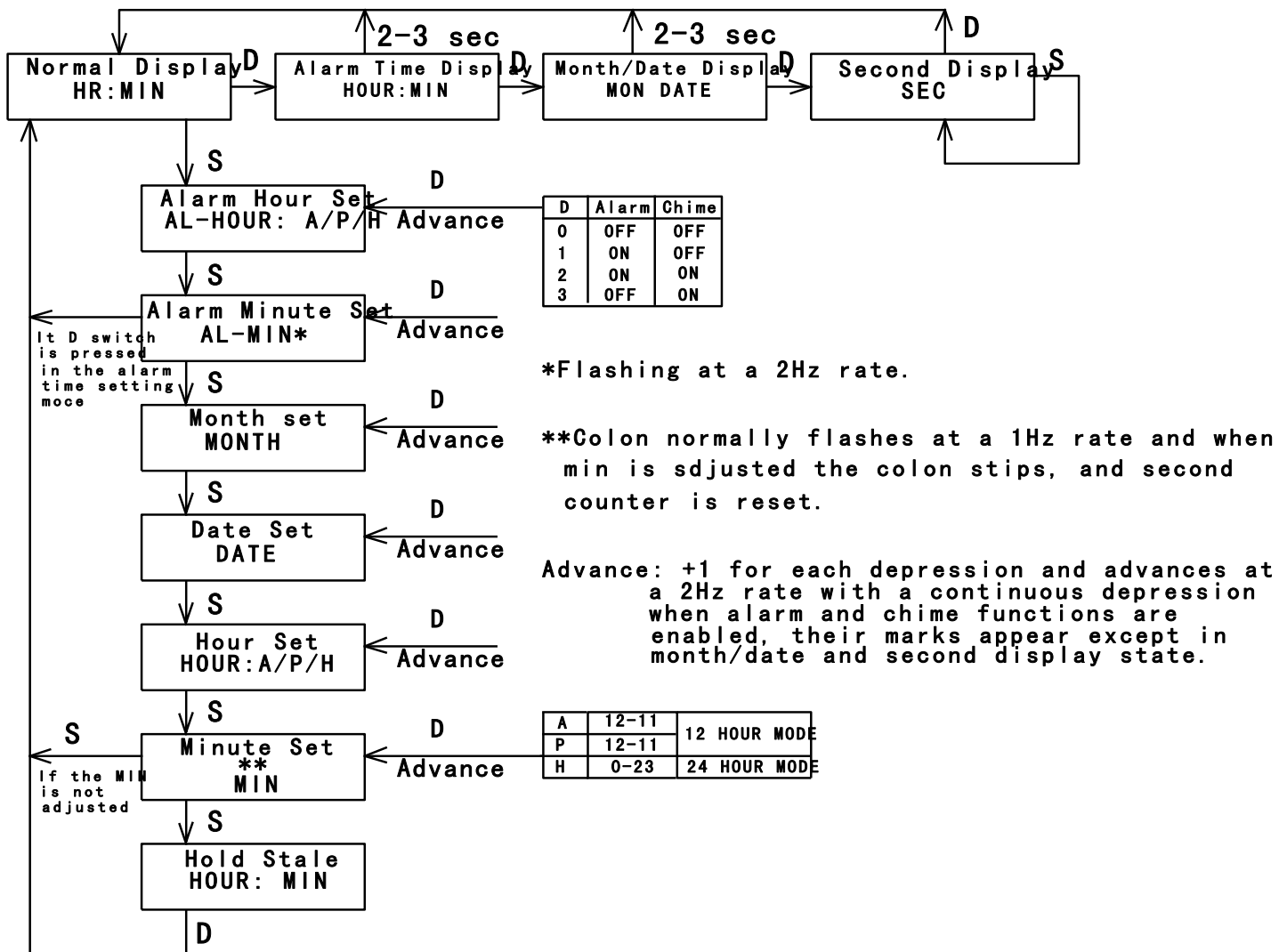


Fig.2

ALARM TIMING DIAGRAM IN THE SNOOZE MODE

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In the existing version the SNOOZE function is executed when the ALARM mode is set (the ALARM icon is turned on). When the current time reaches a preset alarm time a 30sec. Alarm sound is generated (period T1 in Fig.3). Pressing the D

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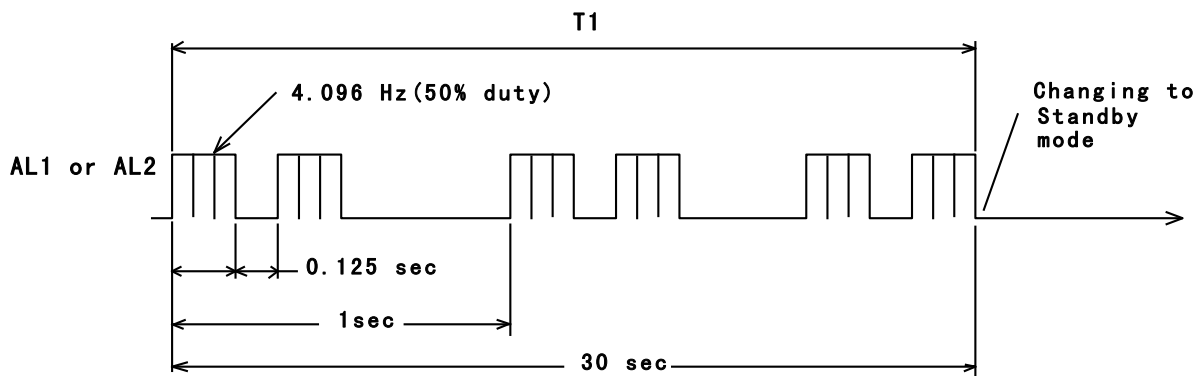
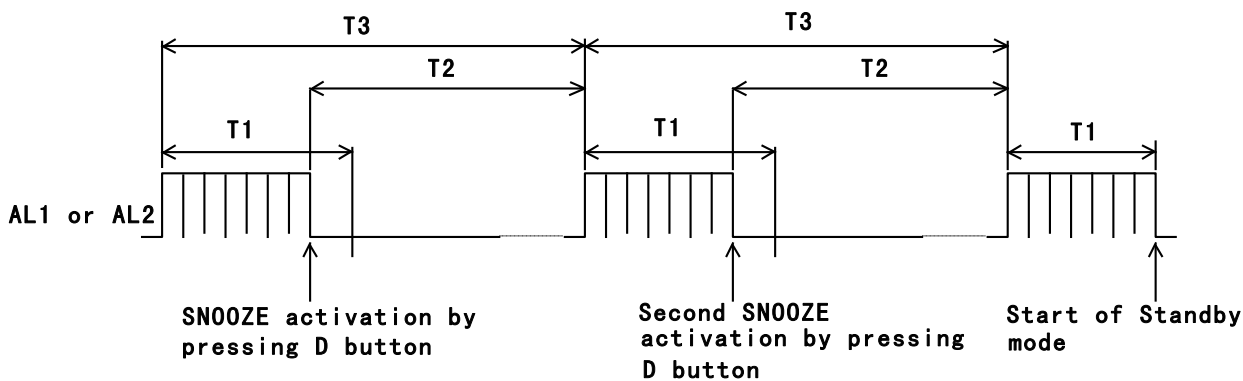


Fig. 3. Alarm timings Diagram

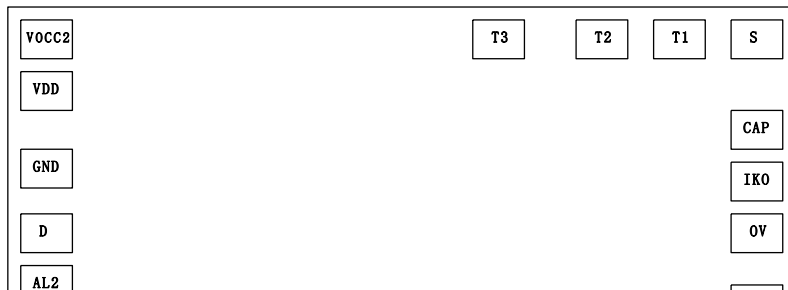


T1 = 30sec, alarm sound duration.

T2 = 4-5min, time to next alarm sound in the SNOOZE mode, ALARM icon flashing.

T3 = 5min.

Fig.4. Alarm timings Diagram in the SNOOZE mode.



IC尺寸:
1100*1100

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